

	Comments	Error Definition	Errors
40			0
41			0
42			0

	Type	Hits	Search Text	DBs	Time Stamp
1	BRS	905	network near chip	USPAT; US-PGPUB; EPO; JPO	2004/05/05 18:48
2	BRS	182	external near logic near (unit device circuit module node adapter card system loadboard)	USPAT; US-PGPUB; EPO; JPO	2004/05/05 18:54
3	BRS	85397	((exten\$4 expan\$4) near (port unit device node card module card))	USPAT; US-PGPUB; EPO; JPO	2004/05/05 18:53
4	BRS	3846	((exten\$4 expan\$4) near (port unit device node card module card)) and packet	USPAT; US-PGPUB; EPO; JPO	2004/05/05 18:53
5	BRS	66	((((exten\$4 expan\$4) near (port unit device node card module card)) and packet) and (network near chip))	USPAT; US-PGPUB; EPO; JPO	2004/05/05 18:53
6	BRS	9533	external with logic with (unit device circuit module node adapter card system loadboard)	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:01
7	BRS	20	(((((exten\$4 expan\$4) near (port unit device node card module card)) and packet) and (network near chip)) and (external with logic with (unit device circuit module node adapter card system loadboard)))	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:02
8	BRS	9	((((((exten\$4 expan\$4) near (port unit device node card module card)) and packet) and (network near chip)) and (external with logic with (unit device circuit module node adapter card system loadboard))) and @ad<20010125	USPAT; US-PGPUB; EPO; JPO	2004/05/05 18:55
9	BRS	4	((((((((exten\$4 expan\$4) near (port unit device node card module card)) and packet) and (network near chip)) and (external with logic with (unit device circuit module node adapter card system loadboard))) and @ad<20010125) and test\$3	USPAT; US-PGPUB; EPO; JPO	2004/05/05 18:55
10	BRS	377458	external with (unit device circuit module node adapter card system loadboard)	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:04
11	BRS	9533	(external with (unit device circuit module node adapter card system loadboard)) and (external with logic with (unit device circuit module node adapter card system loadboard))	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:03

	Type	Hits	Search Text	DBs	Time Stamp
12	BRS	9533	(external with logic with (unit device circuit module node adapter card system loadboard)) and (external with (unit device circuit module node adapter card system loadboard))	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:03
13	BRS	87895	external near (unit device circuit module node adapter card system loadboard)	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:04
14	BRS	26066	(external near (unit device circuit module node adapter card system loadboard)) and test\$3	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:04
15	BRS	2	(((((exten\$4 expan\$4) near (port unit device node card module card)) and packet) and (network near chip)) and (external with logic with (unit device circuit module node adapter card system loadboard))) and ((external near (unit device circuit module node adapter card system loadboard)) and test\$3)	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:05
16	BRS	1	(network near chip) and (external near logic near (unit device circuit module node adapter card system loadboard))	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:11
17	IS&R	1	("5953335").PN.	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:32
18	IS&R	1	("5193110").PN.	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:32
19	IS&R	1	("5231633").PN.	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:33
20	BRS	108994	test\$3 and (expansion extending near (port unit device node circuit device module))	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:36
21	BRS	28924	(test\$3 and (expansion extending near (port unit device node circuit device module))) and (packet frame)	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:36
22	BRS	3468	((test\$3 and (expansion extending near (port unit device node circuit device module))) and (packet frame)) and (external near (device unit node system circuit board loadboard adapter module FPGA))	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:38

	Type	Hits	Search Text	DBs	Time Stamp
23	BRS	53	((((test\$3 and (expansion extending near (port unit device node circuit device module))) and (packet frame)) and (external near (device unit node system circuit board loadboard adapter module FPGA))) and (network near chip)	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:38
24	BRS	33	(((((test\$3 and (expansion extending near (port unit device node circuit device module))) and (packet frame)) and (external near (device unit node system circuit board loadboard adapter module FPGA))) and (network near chip)) and @ad<20010125	USPAT; US-PGPUB; EPO; JPO	2004/05/05 19:39
25	BRS	69609	370/\$.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/05/06 09:27
26	BRS	10952	370/\$.ccls. and test\$3	USPAT; US-PGPUB; EPO; JPO	2004/05/06 09:28
27	BRS	8809	(370/\$.ccls. and test\$3) and @ad<20010125	USPAT; US-PGPUB; EPO; JPO	2004/05/06 09:47
28	BRS	480	((370/\$.ccls. and test\$3) and @ad<20010125) and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))	USPAT; US-PGPUB; EPO; JPO	2004/05/06 09:49
29	BRS	300	((((370/\$.ccls. and test\$3) and @ad<20010125) and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external	USPAT; US-PGPUB; EPO; JPO	2004/05/06 09:49
30	BRS	0	(((((370/\$.ccls. and test\$3) and @ad<20010125) and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))) and (network near chip)	USPAT; US-PGPUB; EPO; JPO	2004/05/06 09:36

	Type	Hits	Search Text	DBs	Time Stamp
31	BRS	12	(((((370/\$.ccls. and test\$3) and @ad<20010125) and ((expans\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))) and (network with chip)	USPAT; US-PGPUB; EPO; JPO	2004/05/06 09:50
32	BRS	29109	(test\$4 same (chip (network near switch\$4))) and @ad<20010125	USPAT; US-PGPUB; EPO; JPO	2004/05/06 09:48
33	BRS	1227	((test\$4 same (chip (network near switch\$4))) and @ad<20010125) and ((expans\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))	USPAT; US-PGPUB; EPO; JPO	2004/05/06 09:49
34	BRS	741	((test\$4 same (chip (network near switch\$4))) and @ad<20010125) and ((expans\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external	USPAT; US-PGPUB; EPO; JPO	2004/05/06 09:50
35	BRS	75	(((((test\$4 same (chip (network near switch\$4))) and @ad<20010125) and ((expans\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))) and (network with chip)	USPAT; US-PGPUB; EPO; JPO	2004/05/06 09:51
36	BRS	321	(((((test\$4 same (chip (network near switch\$4))) and @ad<20010125) and ((expans\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system)))	USPAT; US-PGPUB; EPO; JPO	2004/05/06 10:57

	Type	Hits	Search Text	DBs	Time Stamp
37	BRS	182	(((((test\$4 same (chip (network near switch\$4))) and @ad<20010125) and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))) and (logic near (unit device node loadboard interface circuit board adapter module)))	USPAT; US-PGPUB; EPO; JPO	2004/05/06 10:59
38	BRS	80	(((((test\$4 same (chip (network near switch\$4))) and @ad<20010125) and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))) and (logic near (unit device node loadboard interface circuit board adapter module))) and (FPGA (gate near array)))	USPAT; US-PGPUB; EPO; JPO	2004/05/06 11:00
39	BRS	51	(((((((((test\$4 same (chip (network near switch\$4))) and @ad<20010125) and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))) and (logic near (unit device node loadboard interface circuit board adapter module))) and (FPGA (gate near array))) and (measur\$3 detect\$3 verif\$3 evaluat\$3)) and (DA (destination near address)) and (SA (source near address)))	USPAT; US-PGPUB; EPO; JPO	2004/05/06 11:03

	Type	Hits	Search Text	DBs	Time Stamp
40	BRS	50	(((((test\$4 same (chip (network near switch\$4))) and @ad<20010125) and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))) and (logic near (unit device node loadboard interface circuit board adapter module))) and (FPGA (gate near array))) and (measur\$3 detect\$3 verif\$3 evaluat\$3)) and (DA (destination near address)) and (SA (source near address))) and (swap\$4 exchang\$3)	USPAT; US-PGPUB; EPO; JPO	2004/05/06 11:04
41	BRS	80	(((((test\$4 same (chip (network near switch\$4))) and @ad<20010125) and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))) and (logic near (unit device node loadboard interface circuit board adapter module))) and (FPGA (gate near array))) and (measur\$3 detect\$3 verif\$3 evaluat\$3)	USPAT; US-PGPUB; EPO; JPO	2004/05/06 11:28
42	BRS	117	((((370/\$.ccls. and test\$3) and @ad<20010125) and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))	USPAT; US-PGPUB; EPO; JPO	2004/05/06 12:19